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SUPPLEMENTAL AMENDMENT UNDER 37 C.F.R. § 1.116
EXPEDITED PROCEDURE
GROUP 2816
PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q65962

Katsuji KIMURA

Appln. No.: 09/940,472

Group Art Unit: 2816

Confirmation No.: 4891

Examiner: Minh T. NGUYEN

Filed: August 29, 2001

For: LINEAR VOLTAGE SUBTRACTOR/ADDER CIRCUIT AND MOS DIFFERENTIAL AMPLIFIER CIRCUIT THEREFOR

SUPPLEMENTAL AMENDMENT UNDER 37 C.F.R. § 1.116

MAIL STOP AF

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

In response to the Advisory Action dated July 26, 2004, and further in response to the Office Action dated April 13, 2004, the time for response having been extended to August 13, 2004 by concurrent filing of a petition and payment of extension of time fee, please amend the above-identified application as follows on the accompanying pages.

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